PHILIPS FIRST TO OFFER 80C51 MICROCONTROLLER COMBINING 64K FLASH MEMORY AND 8K RAM

Sunnyvale, Calif., May, 2001
Electronics (NYSE: PHG, AEX: PHI), today announced the 89C668, the first 80C51-based microcontroller to combine up to 64 Kbytes of flash program memory and 8 Kbytes of RAM. The 89C668 completes the 89C66x microcontroller family that is designed for a wide range of applications in the communications, industrial and consumer market segments. The 89C66x microcontroller is most beneficial for design engineers currently using the 80C51 architecture because it reduces time to market and eliminates the cost of upgrading to a different architecture.

64 Kbytes of flash program memory and 8 Kbytes of RAM make the 89C668 ideal for applications written in high-level languages such as C and C++. The flash memory supports both In-System Programming (ISP) and In-Application Programming (IAP) by allowing the program to be updated while it is running. The 89C668 has an embedded I^2C serial bus interface, programmable counter array and watchdog timer. It is well suited for Intelligent Platform Management Interface (IPMI) applications in servers where I^2C serves as the physical protocol for the interface. Because it operates at 6-clocks per machine cycle, other designs that benefit from higher performance and improved electromagnetic compatibility (EMC) are best suited for the 89C668.

The 89C668 microcontroller is available now in PLCC and LQFP packages. The P89C668HBBD, P89C668HBA, and P89C668HFA are priced at $6.40, $6.50, and $7.50, respectively, in quantities of 500. The 89C668 is the latest addition to Philips Semiconductors' recently announced 89C66x microcontroller family (the 89C660, the 89C662, and the 89C664), which are also available now.

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Editorial Contact:
Yen Nguyen, Philips Semiconductors
(408) 474-5100 or yen.nguyen@philips.com

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